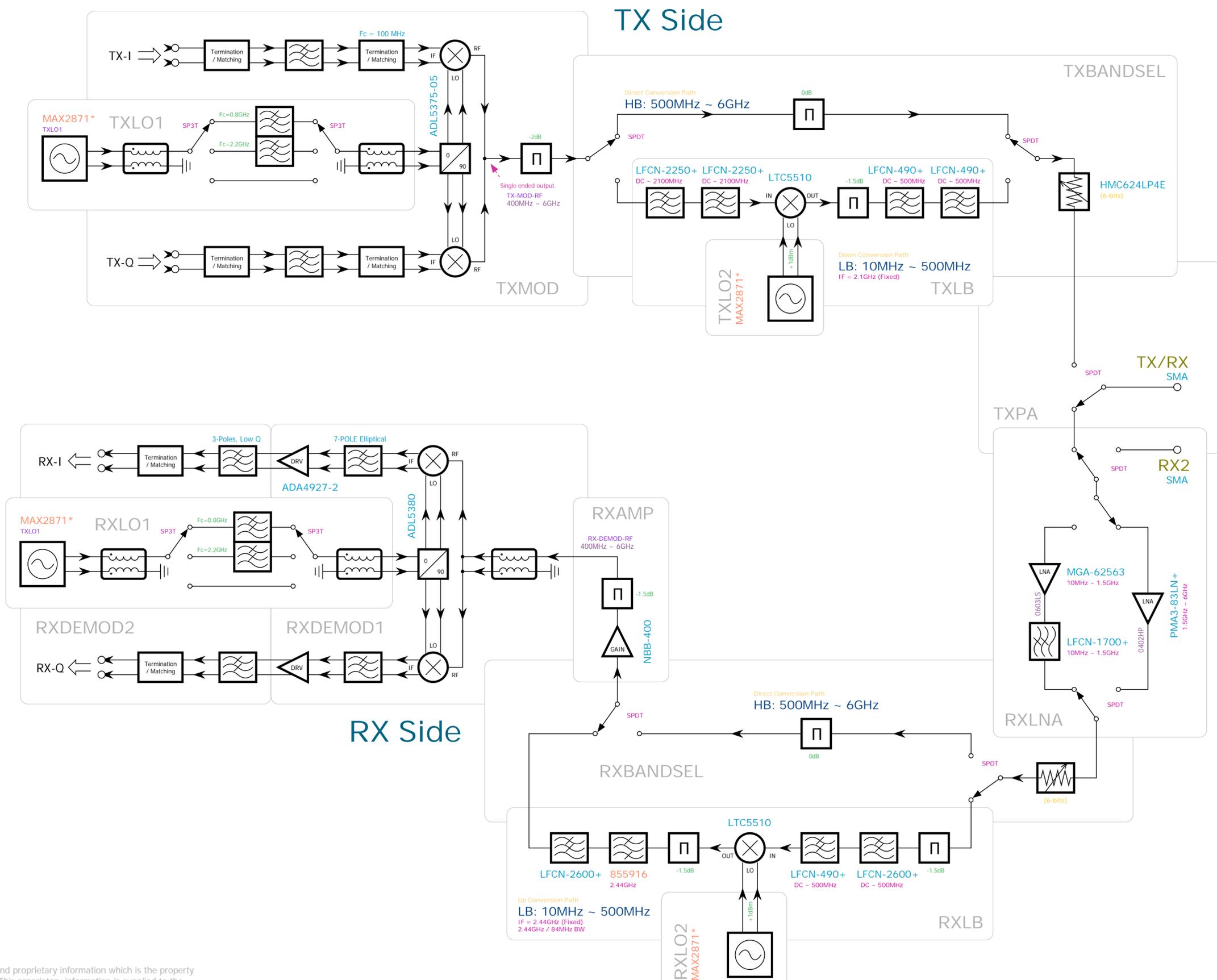


REV	Description
1	INITIAL PRODUCTION VERSION
2	NO ENGINEERING CHANGE, ONLY System Revision Compliance Update
3	Removed D7 and R52 to make U6 permanently "ON"
B	Relocated F3 / F26 to avoid conflict to the stand screw / Fixed Ettus Logo Size
C	Replaced VMMK LNA with PMA3 LNA, High Isolation T/R Switch

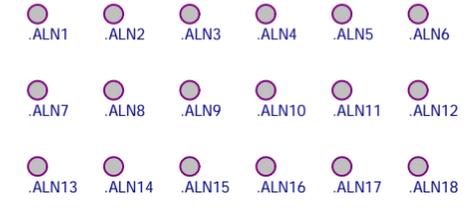
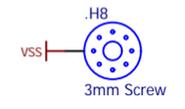
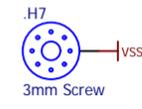
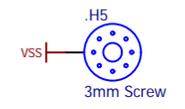
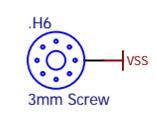
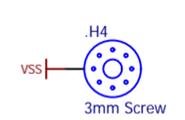
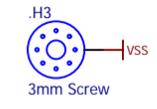
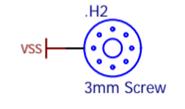
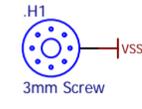
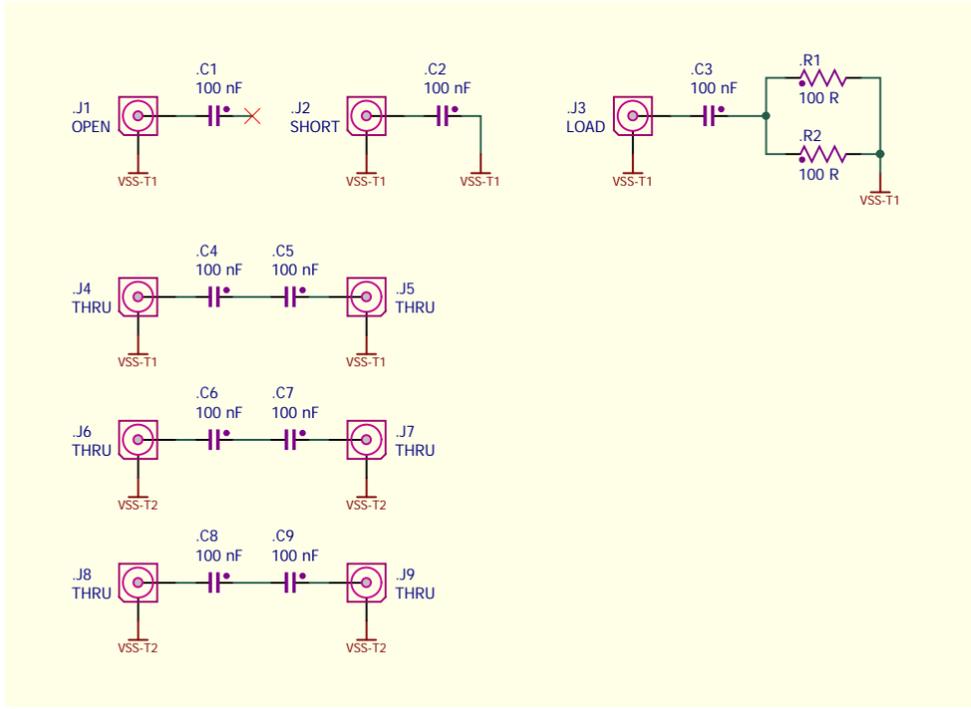


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Design Team		Ettus Research		4600 Patrick Henry Drive Santa Clara, CA 95054 USA	
DRAWN BY	Shyu Lee (Xu Li)	DATE	03/19/2015		
REVISOR BY	-	DATE	-		
VERIFIED BY	Matt Ettus / Jon Kiser	System Block Diagram			
TECHNICIAN	Erica Magers	SCH_UBX_USRP_DAUGHTERBOARD_LP_160MHZ_BW_10MHZ-6GHZ			
ENGINEER	Shyu Lee (Xu Li)	SIZE	D	DOC CODE	7U296
		DRAWING PROJECT NO	158278C-03	REV	1
		FILE NAME	01_BlockDiagram_SchDoc	DATE	10/24/2016
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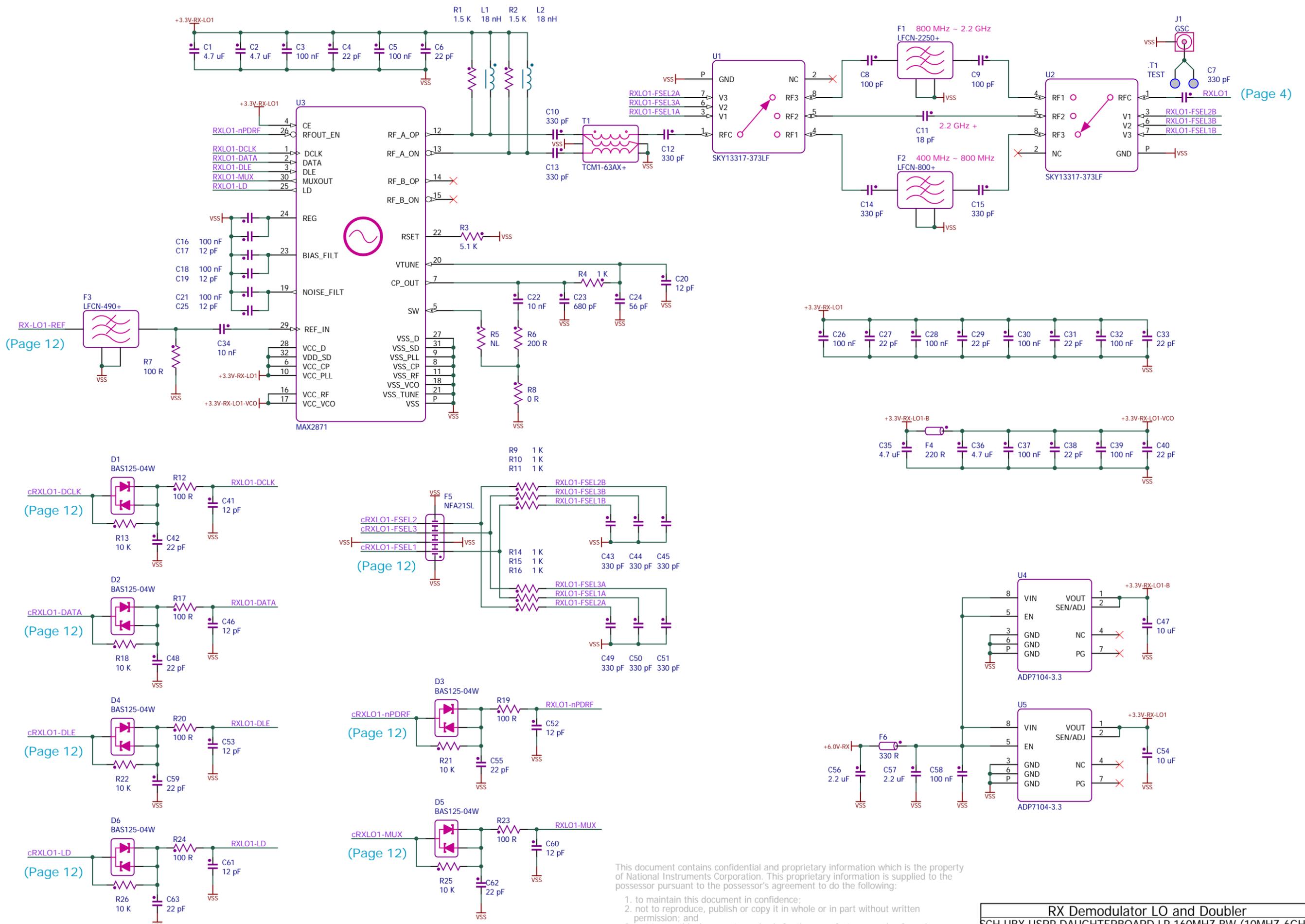


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Essential Hardware Elements			
SCH,UBX USRP DAUGHTERBOARD,LP,160MHZ BW (10MHZ-6GHZ)			
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C	7U296	158278C-03	1
FILE NAME	02_EssentialElements.SchDoc	DATE	10/24/2016 SHEET 2 OF 13



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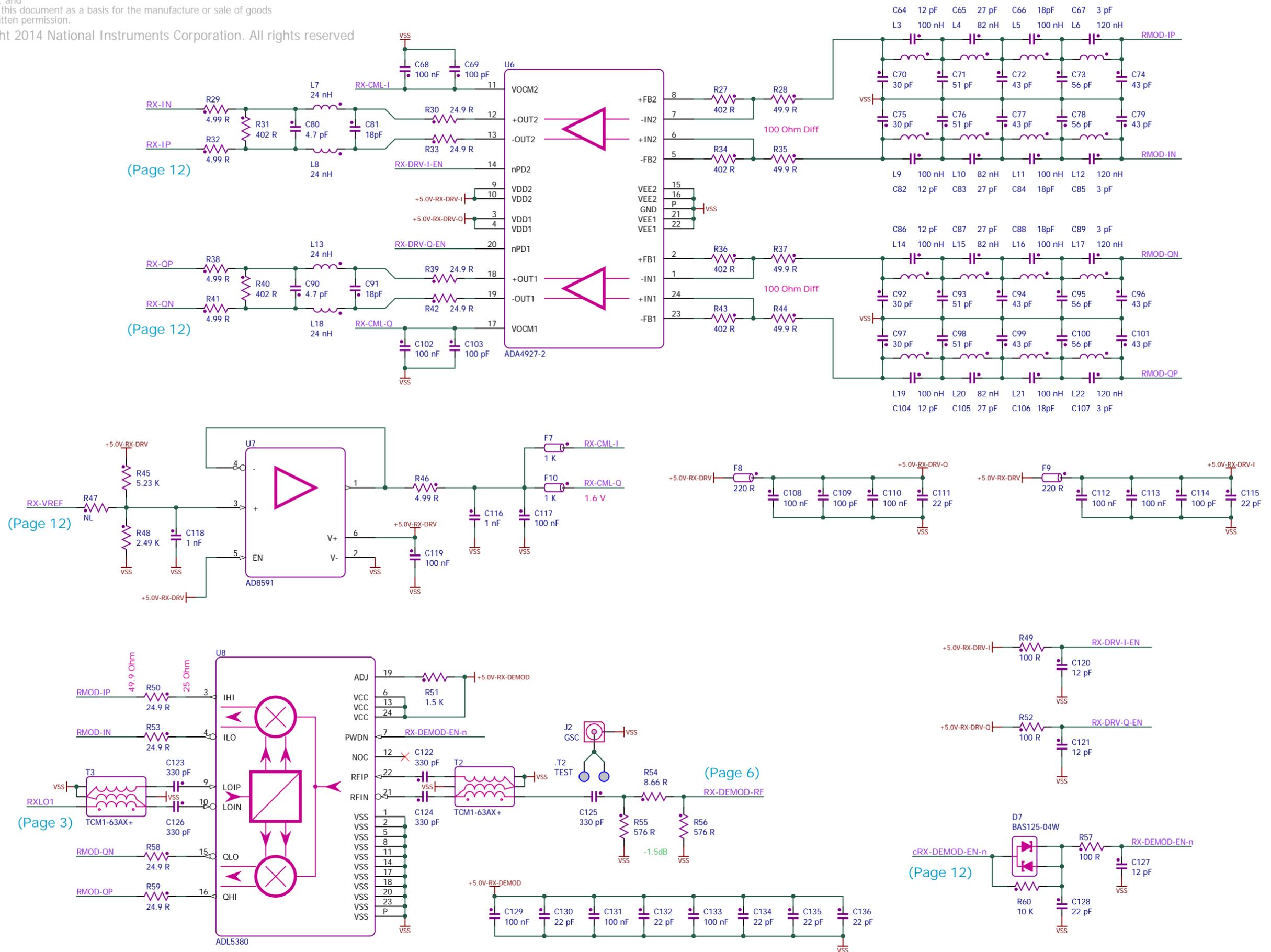
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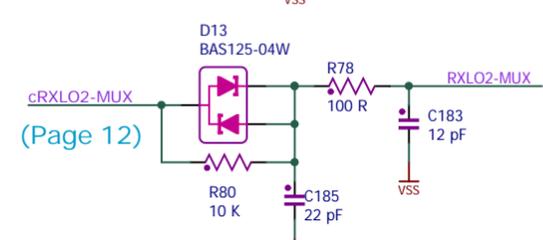
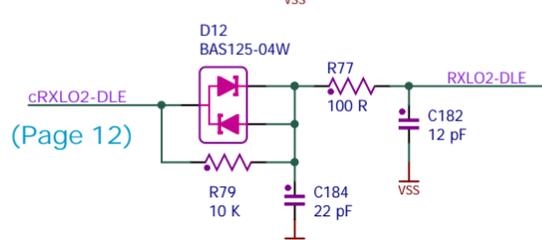
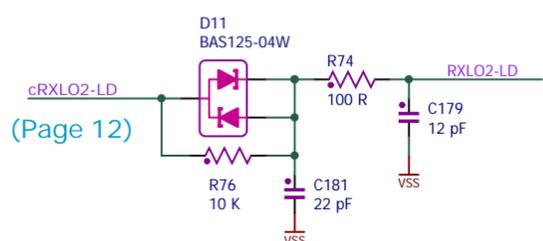
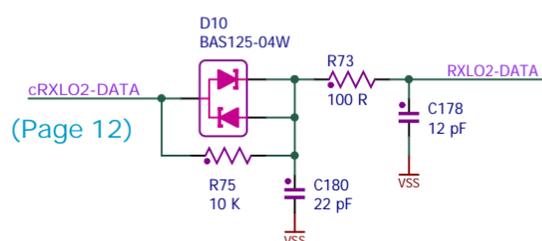
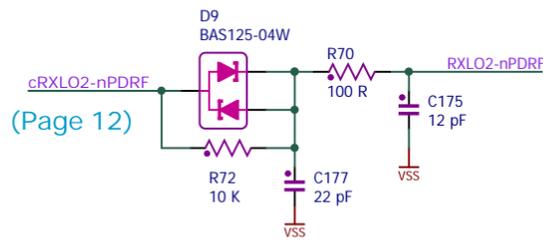
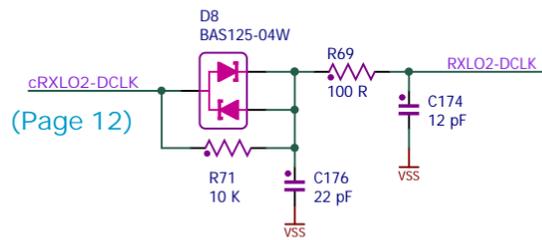
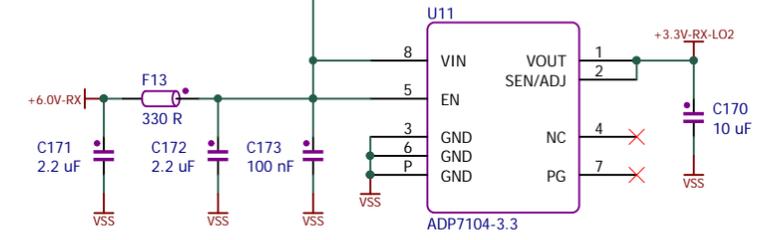
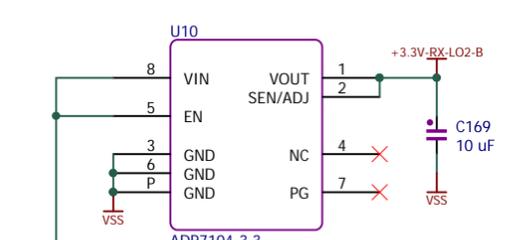
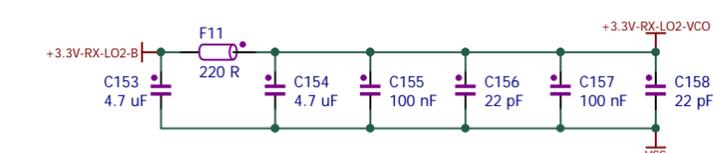
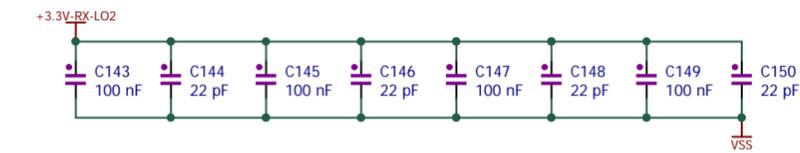
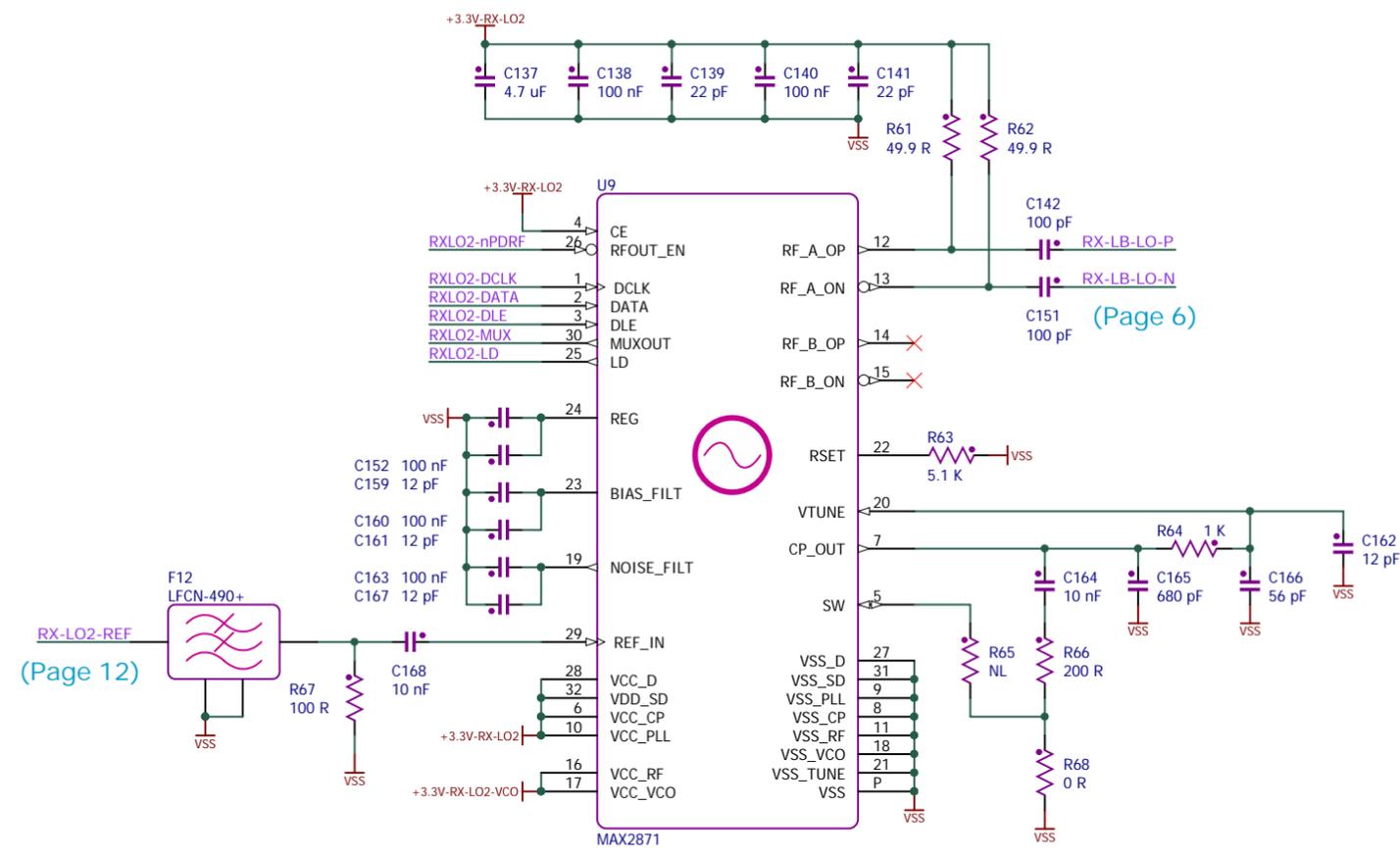


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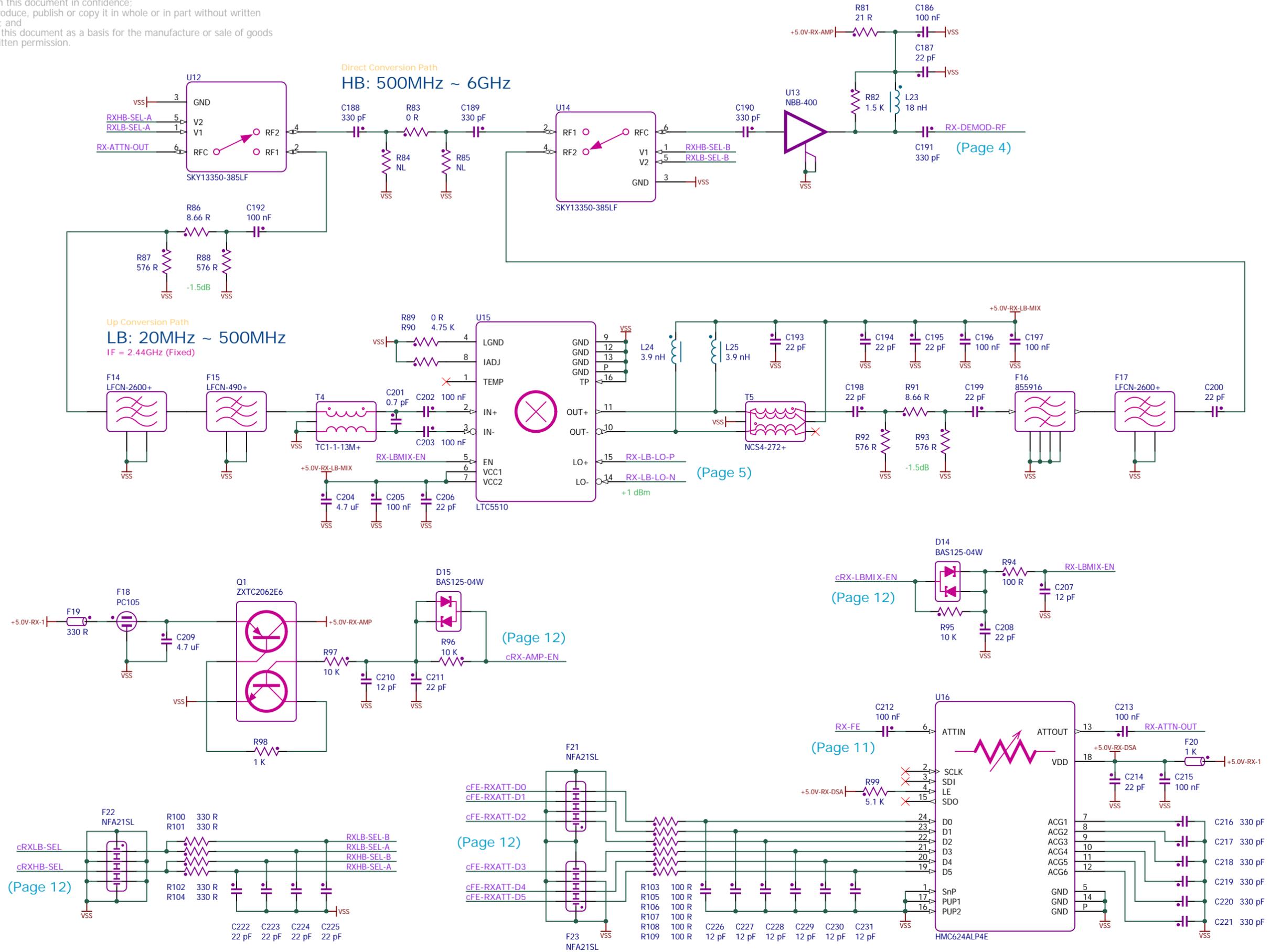
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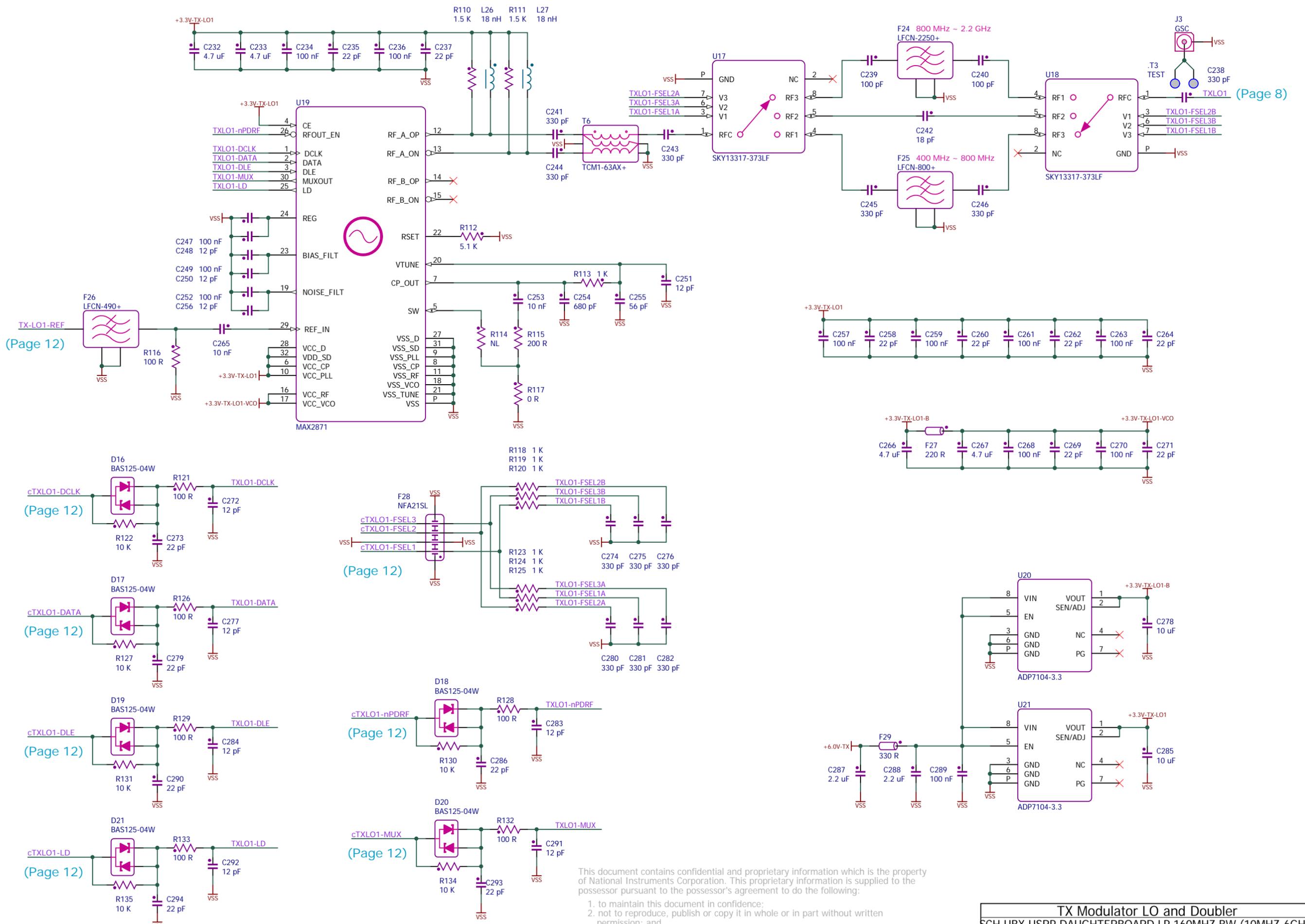
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RX Low Band LO PLL and Mixer Driver			
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		SHEET	5 OF 13

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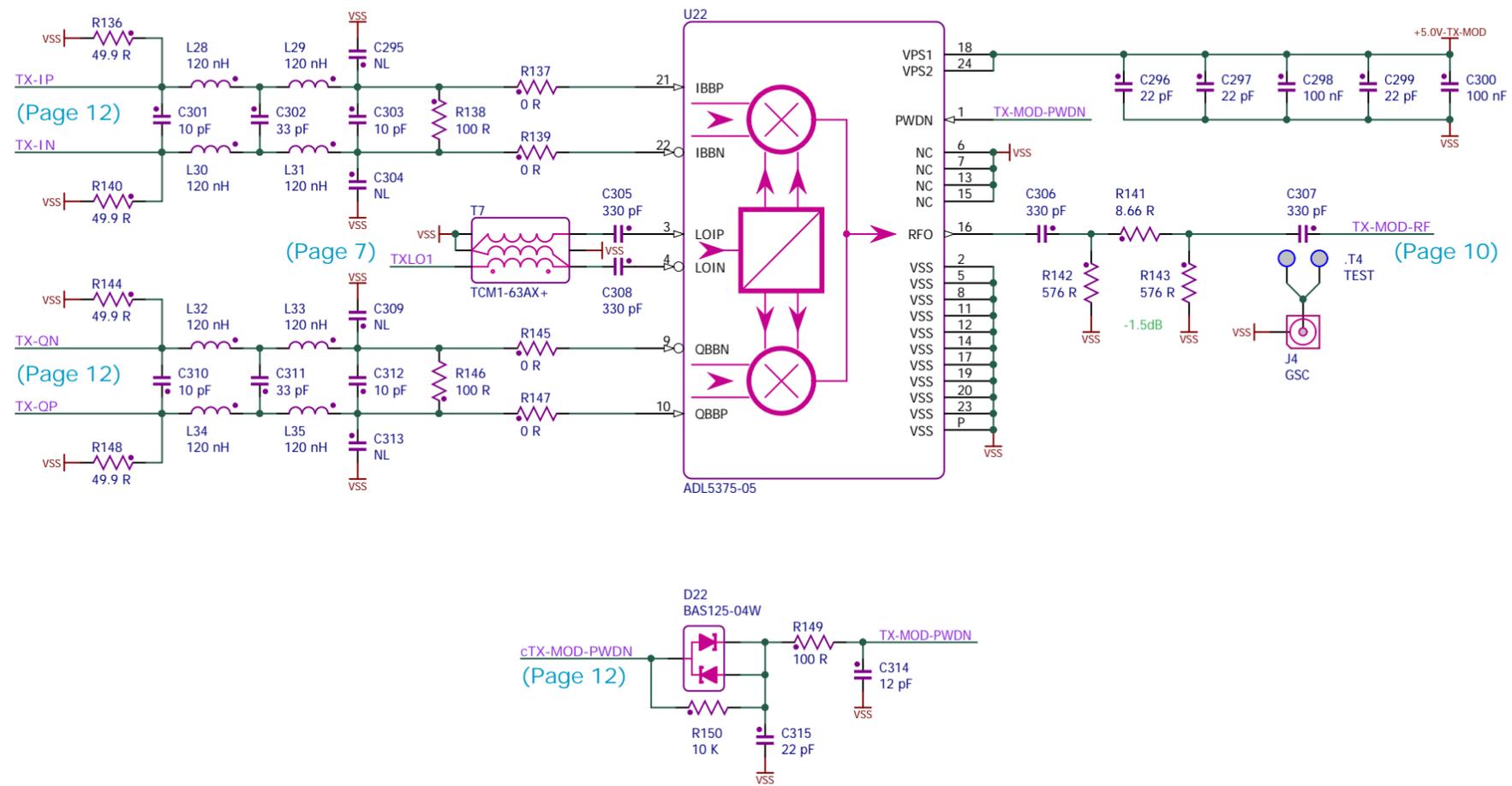
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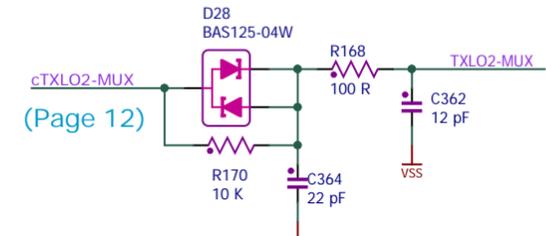
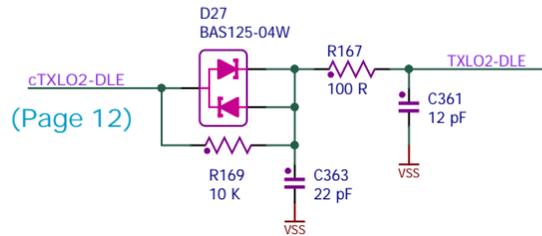
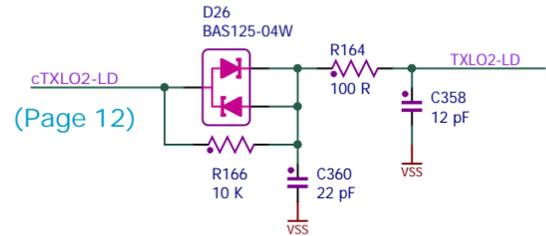
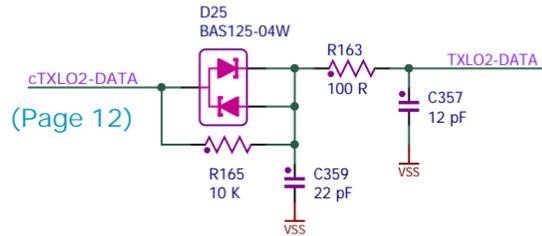
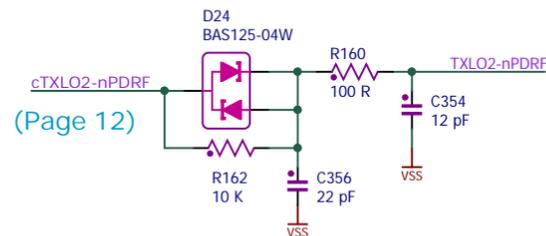
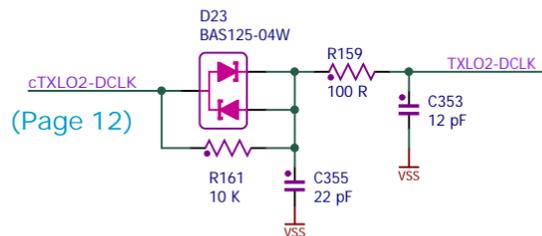
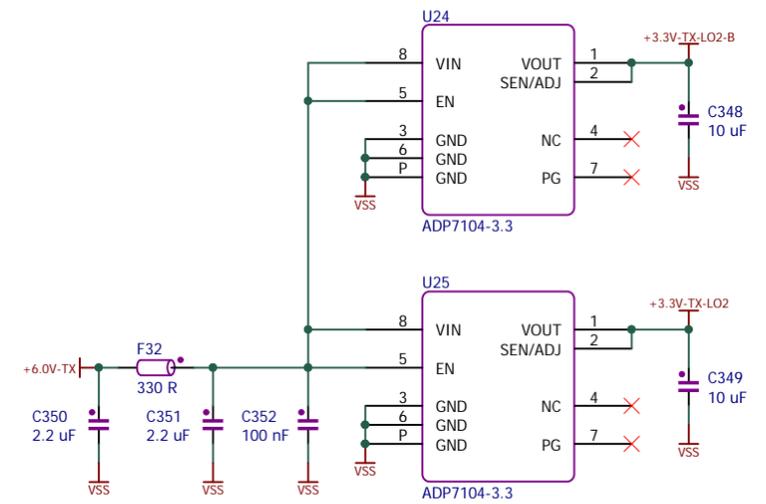
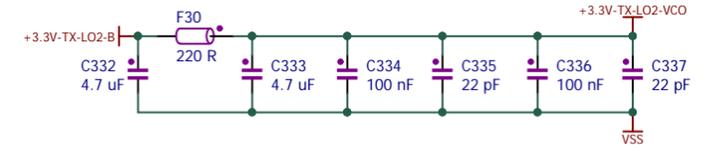
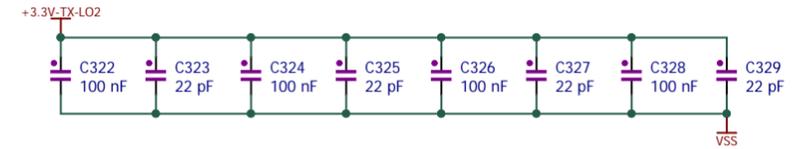
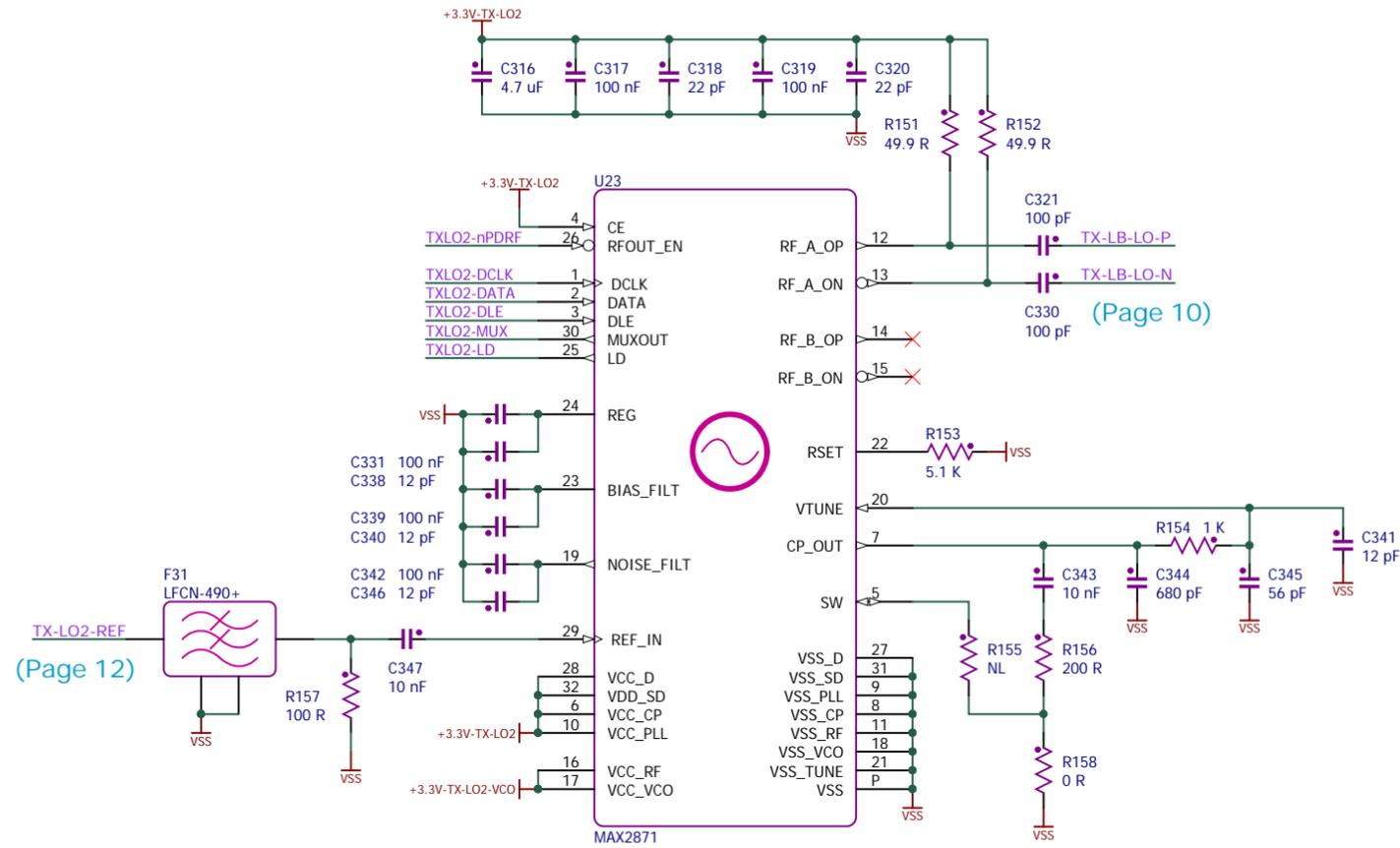
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TX Modulator			
SCH,UBX USRP DAUGHTERBOARD,LP,160MHZ BW (10MHZ-6GHZ)			
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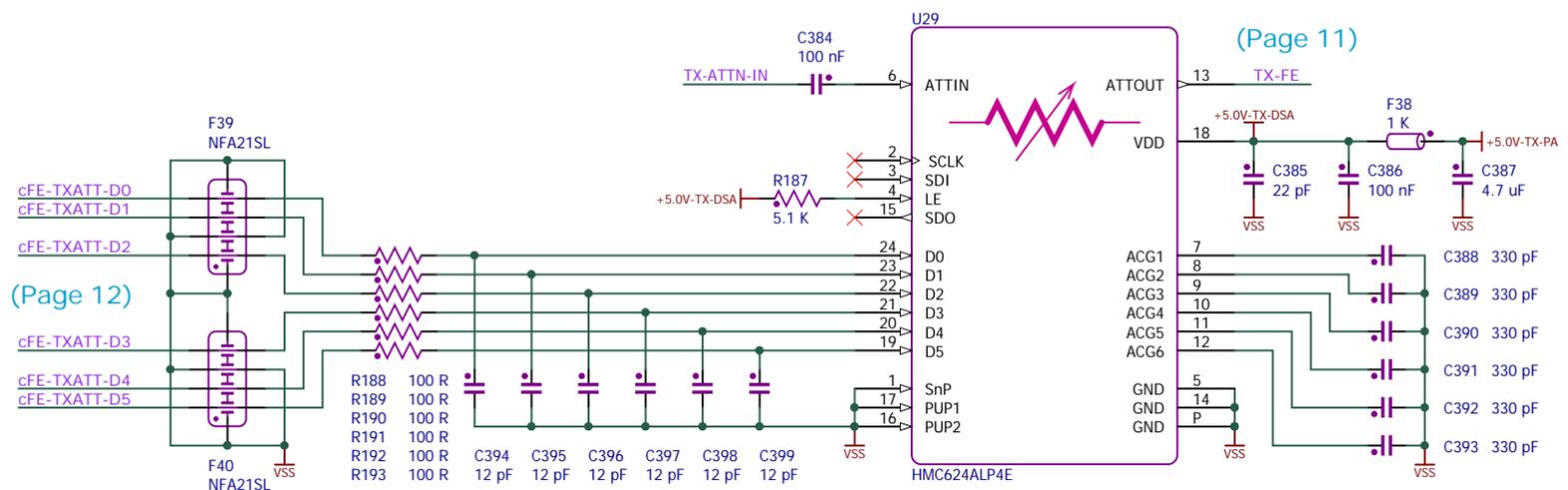
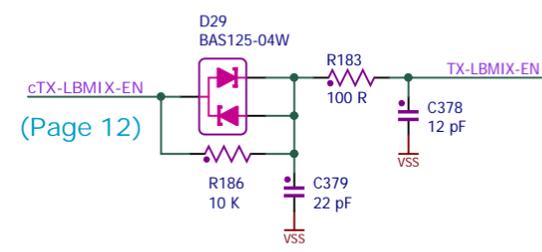
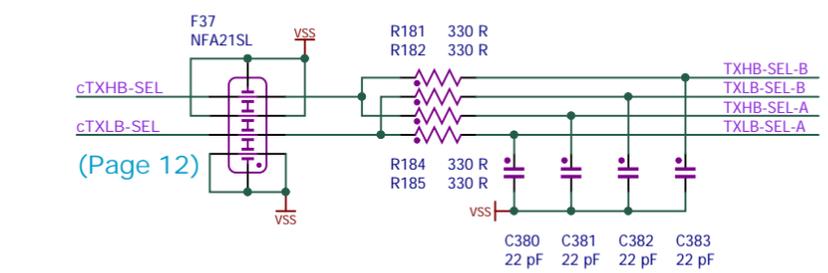
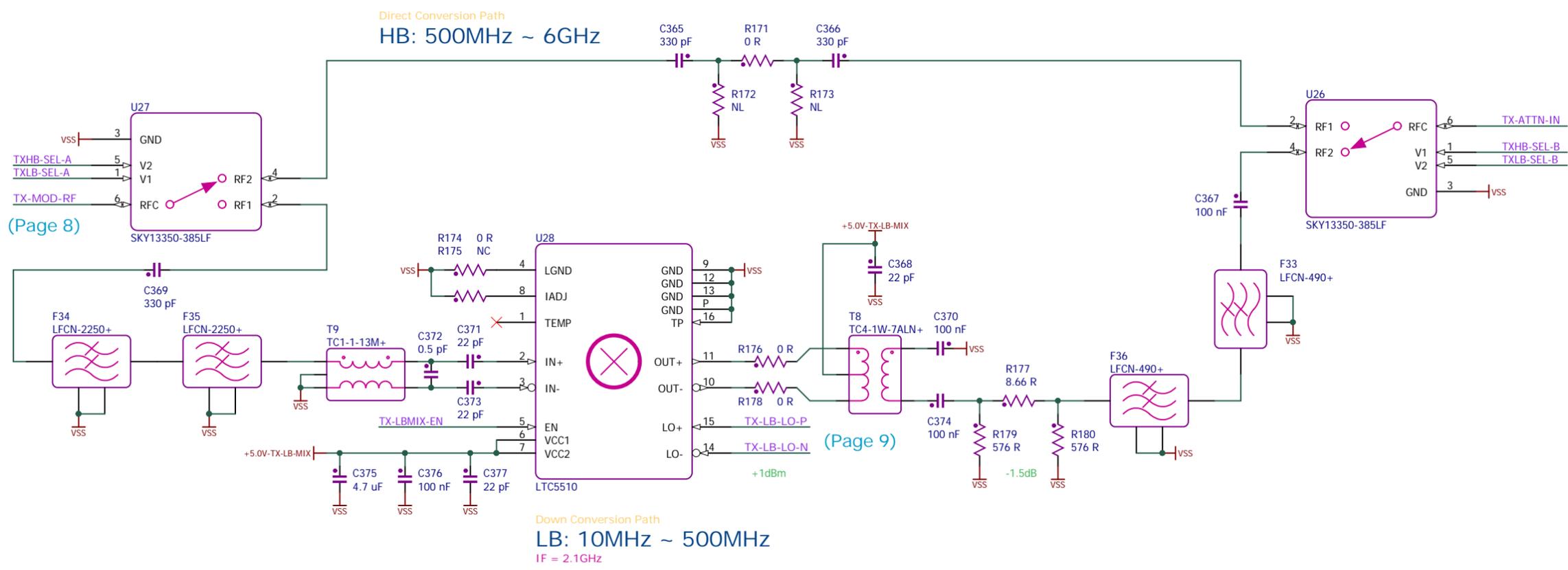


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RX Low Band LO PLL and Mixer Driver			
SCH,UBX USRP DAUGHTERBOARD,LP,160MHZ BW (10MHZ-6GHZ)			
SIZE	DOC CODE	DRAWING PROJECT NO	REV
C	7U296	158278C-03	1
FILE NAME	09_TX_LO2.SchDoc	DATE	10/24/2016
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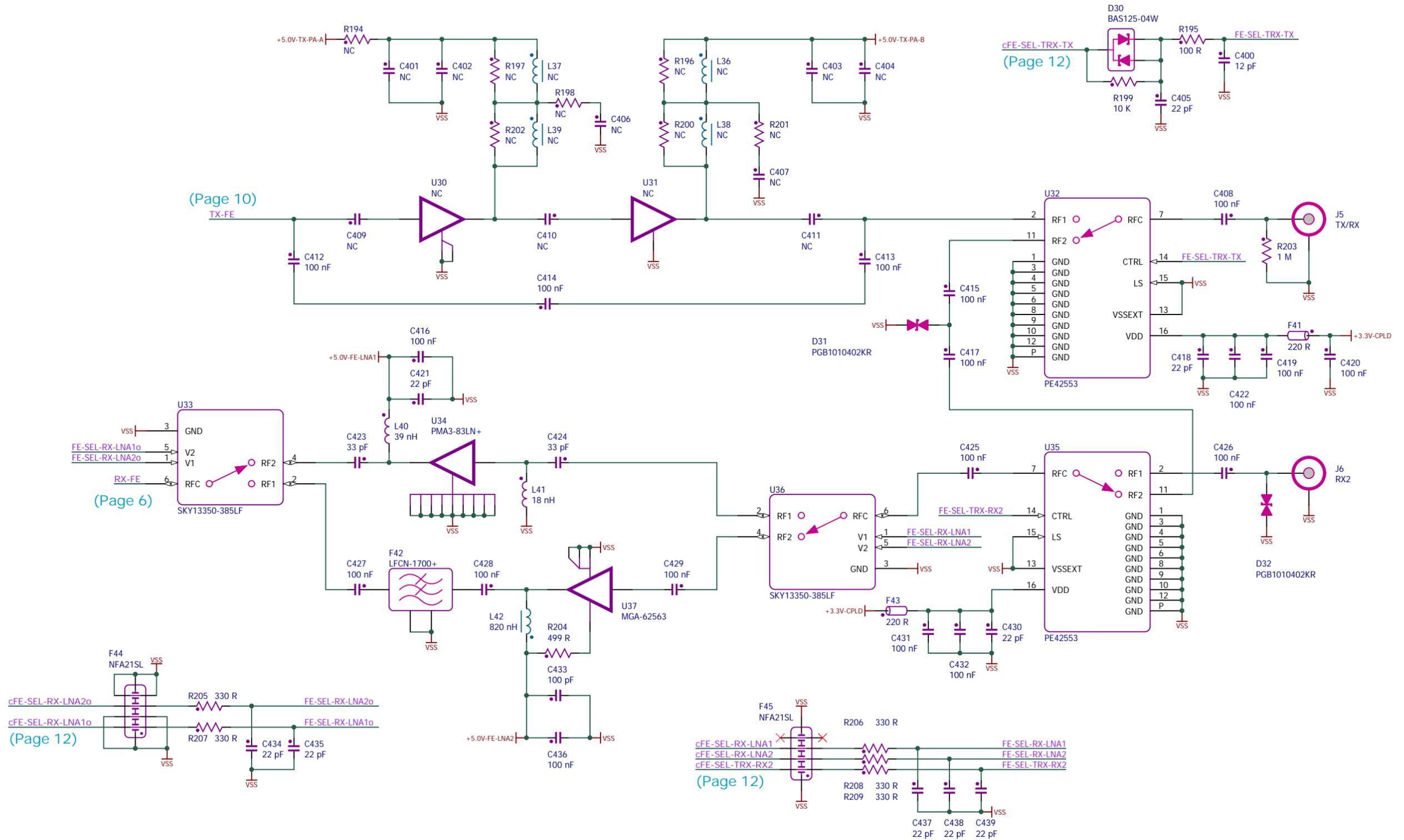
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TX Band Selector and Low Band Mixer			
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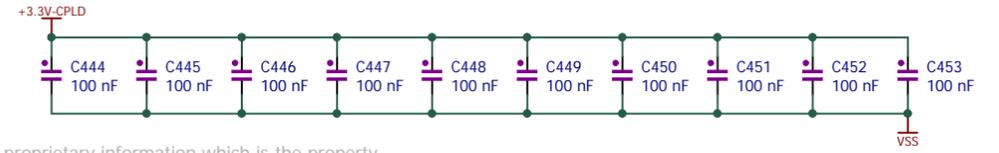
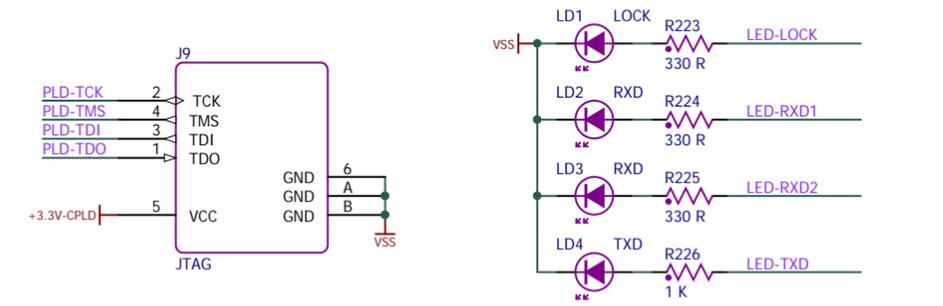
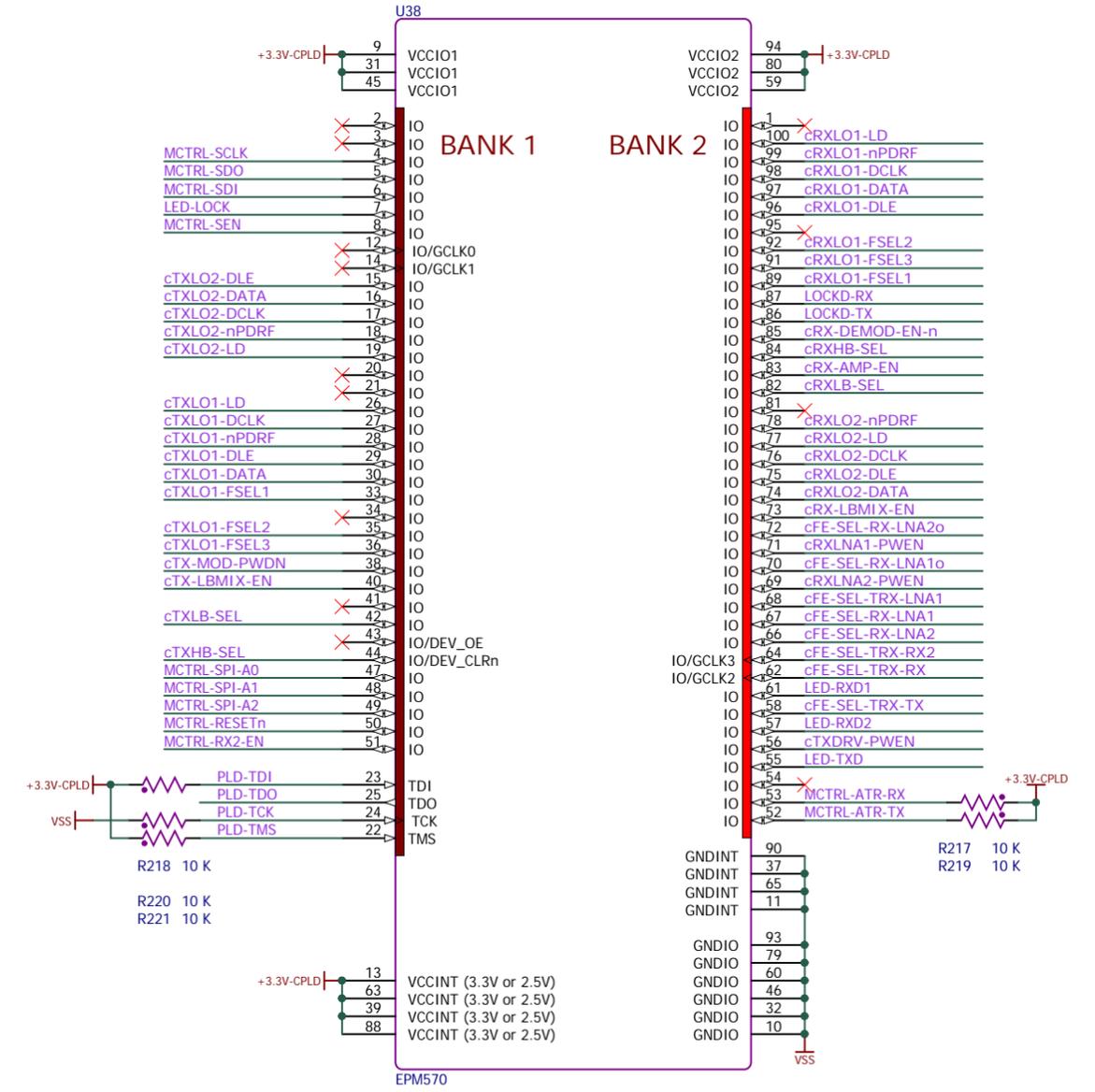
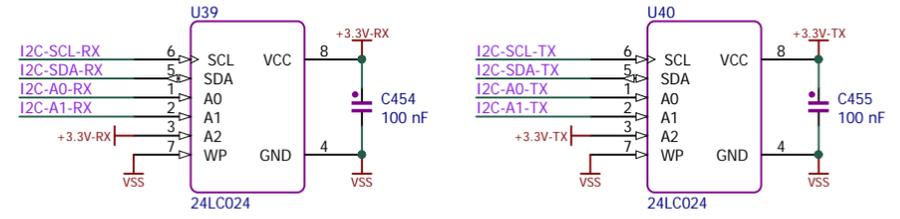
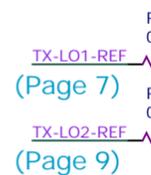
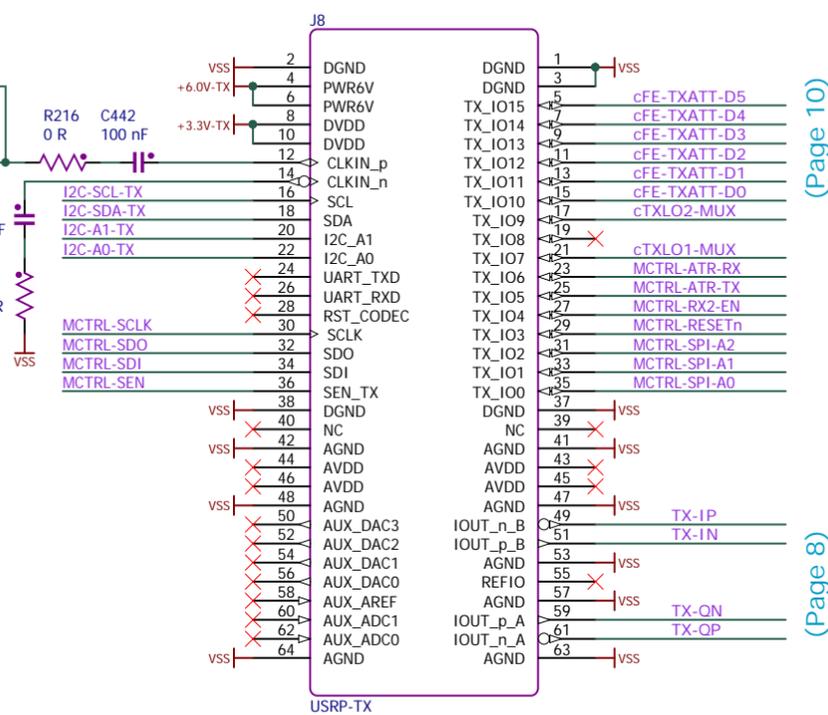
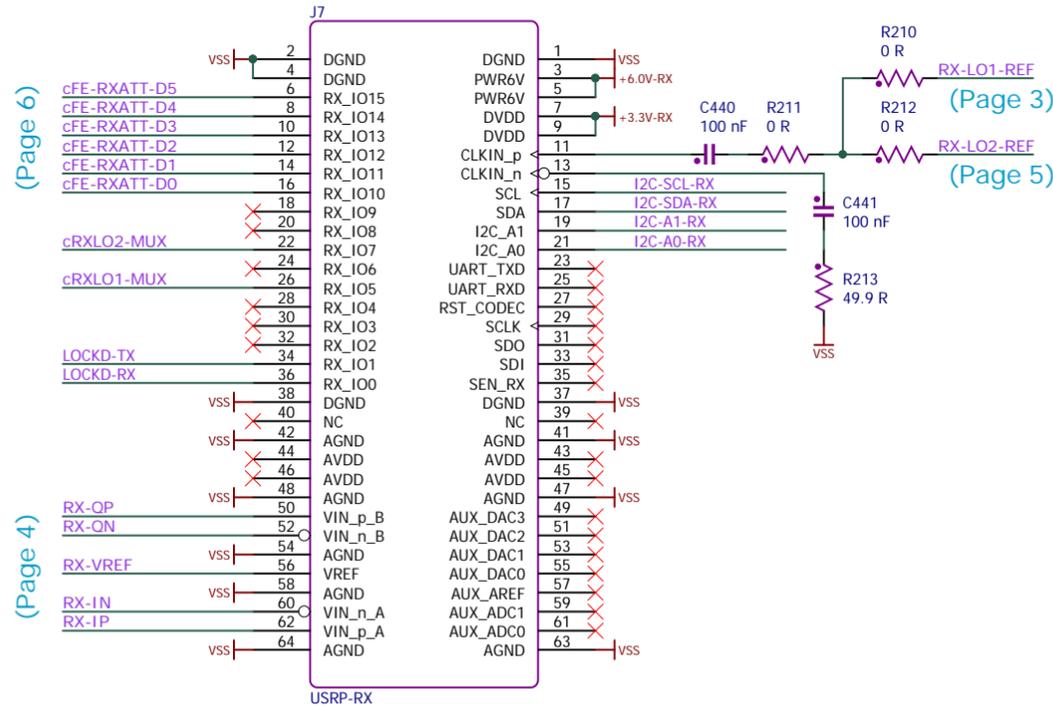
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RF Frontend			
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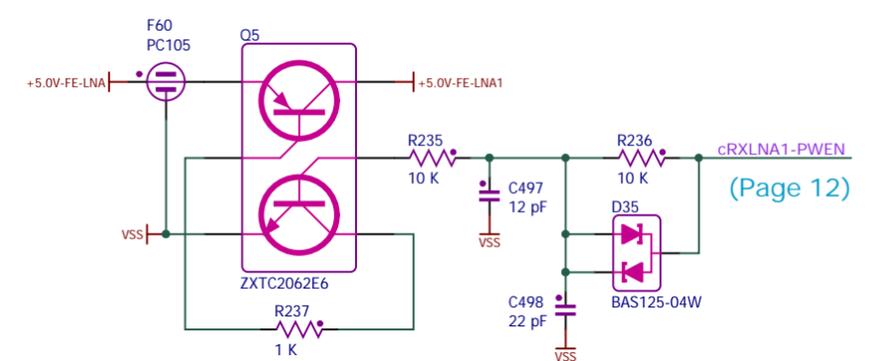
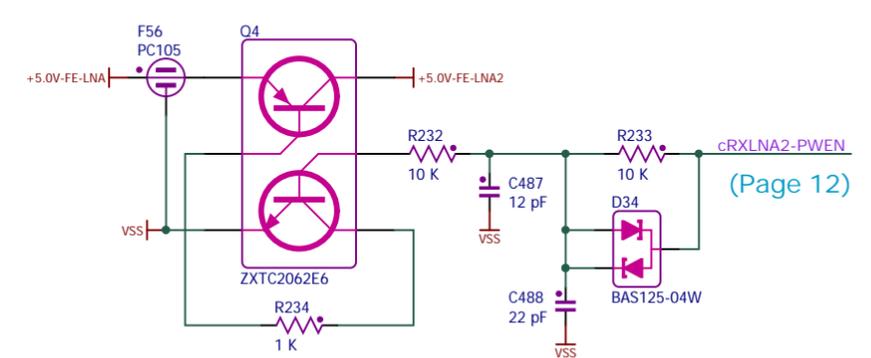
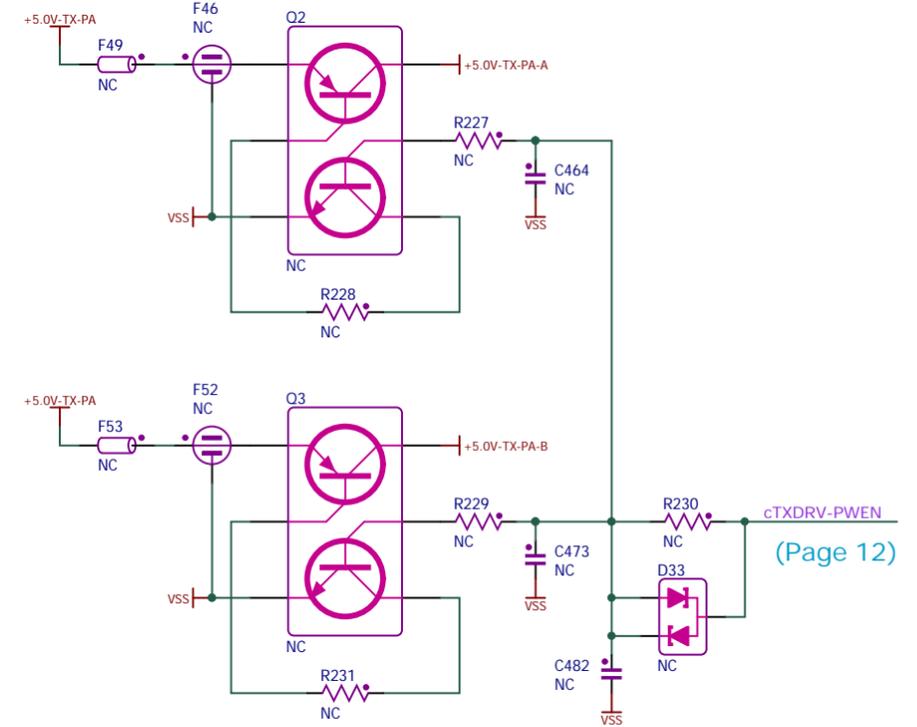
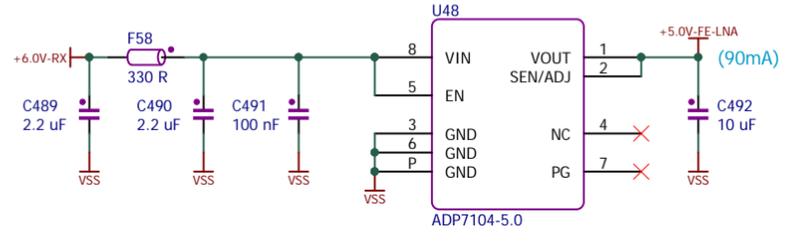
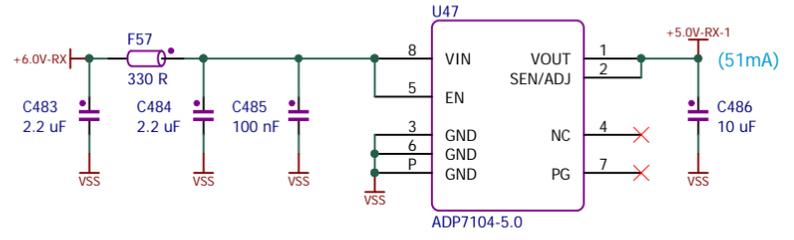
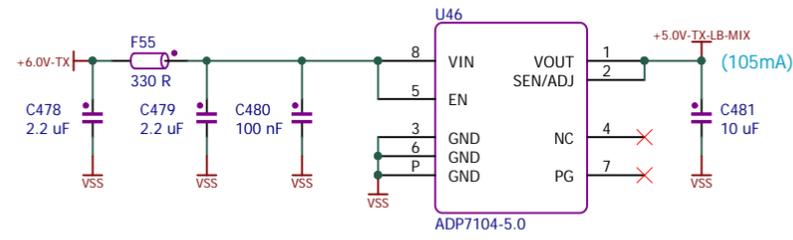
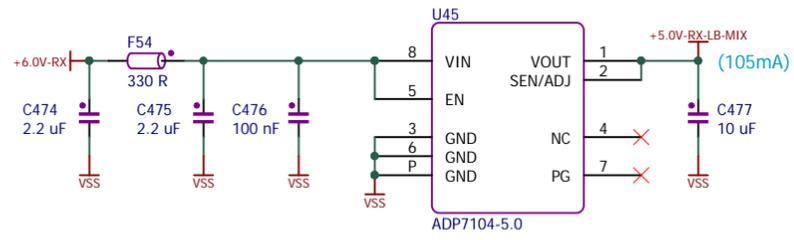
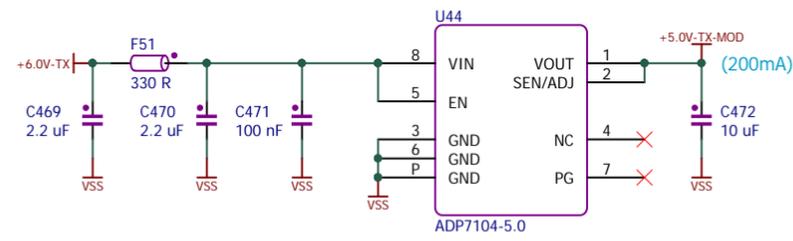
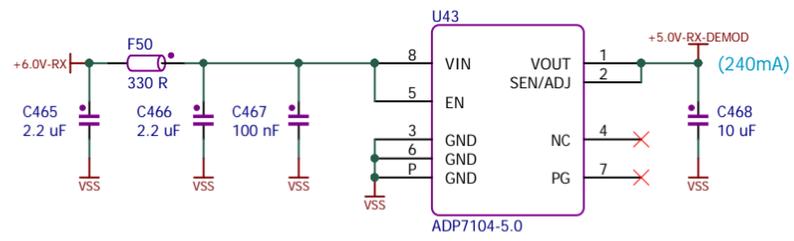
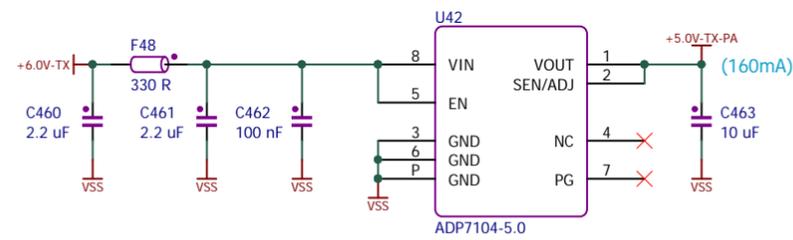
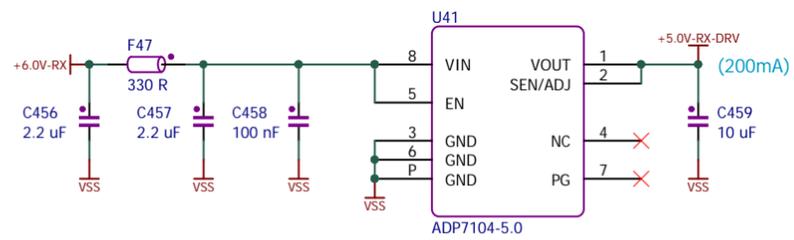


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USRP Standard Daughter Board Elements			
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C	7U296	158278C-03	1
FILE NAME	12_USRP_IO.SchDoc		DATE 10/24/2016 SHEET 12 OF 13



cTXDRV-PWEN (Page 12)

cRXLNA2-PWEN (Page 12)

cRXLNA1-PWEN (Page 12)

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Power Supply			
SCH,UBX USRP DAUGHTERBOARD,LP,160MHZ BW (10MHZ-6GHZ)			
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