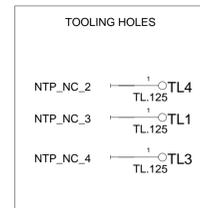
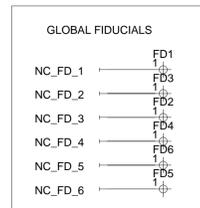
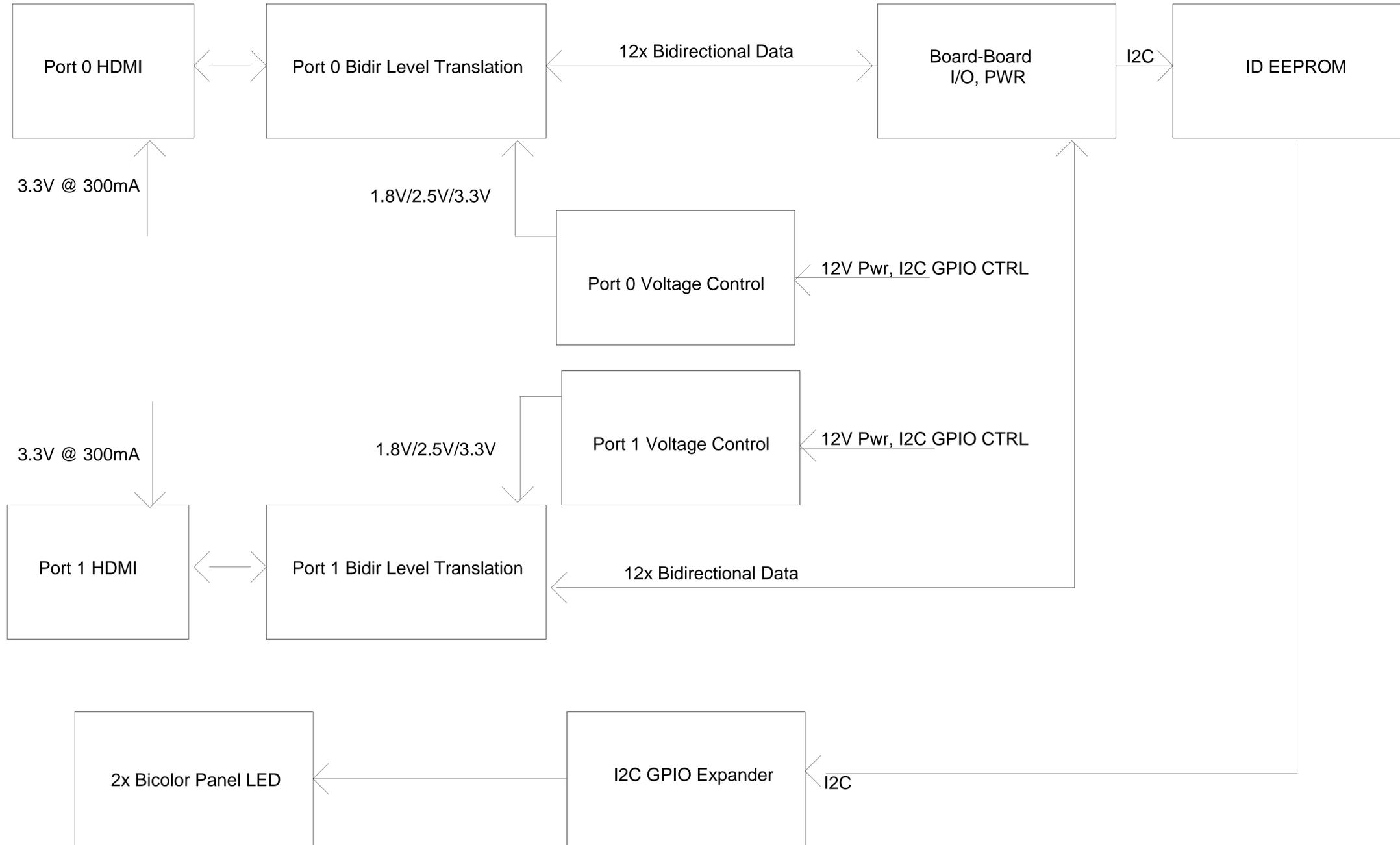


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REVISIONS	
REV	DESCRIPTION
2	REFER TO DATABASE FOR DWG STATUS



DESIGN TEAM	
DRAWN	DATE
REVISED	DATE
CHECKED	
TECH	
ENGR	

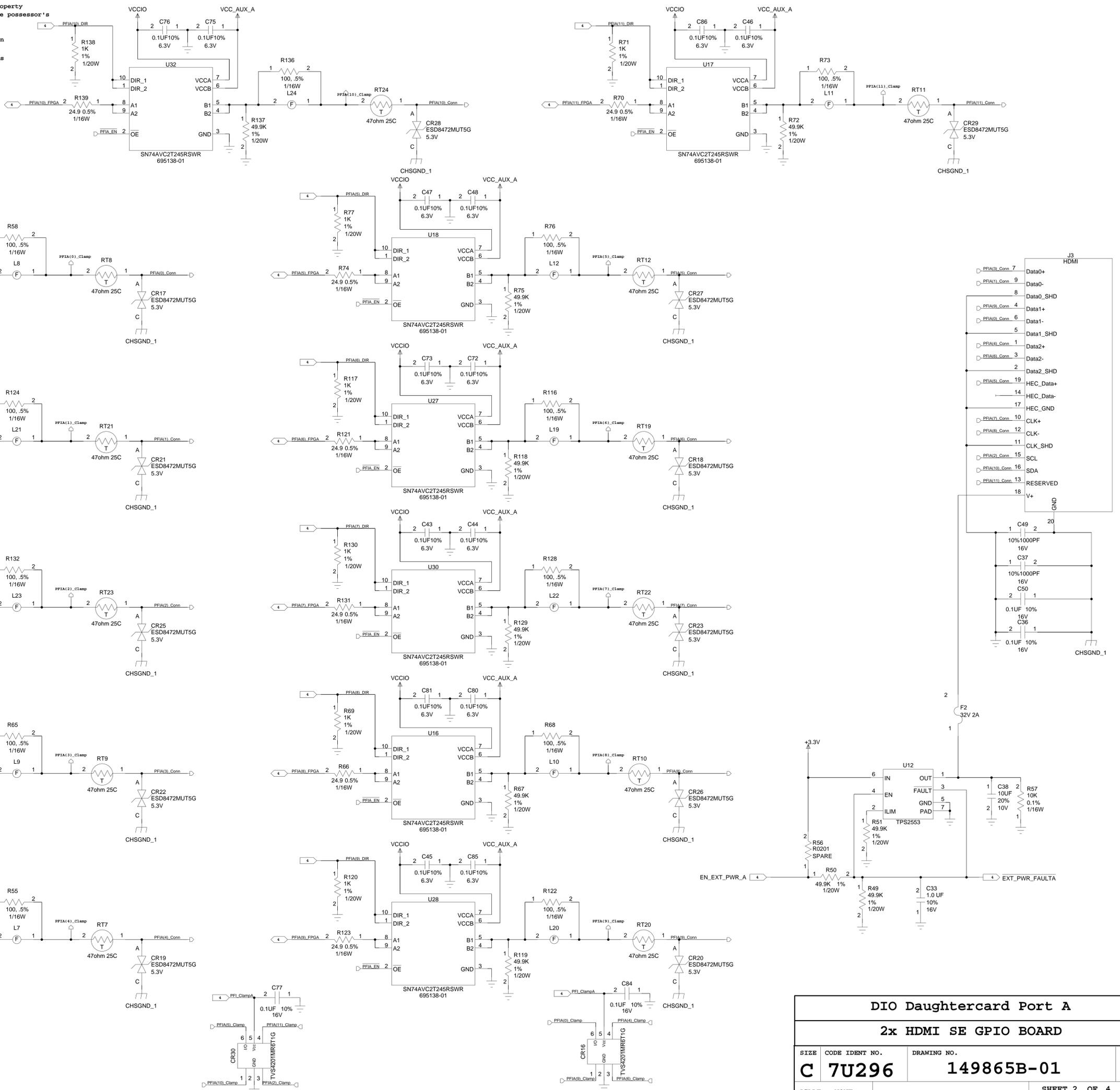
		NI.COM	
		AUSTIN, TEXAS	
TITLE			
<h1>SCHEMATIC DIAGRAM</h1> <h2>USRP DIO AUXILIARY BOARD, SE, 2x12</h2>			
SIZE	CODE IDENT NO.	DRAWING NO.	REV
C	7U296	149865B-01	2
SCALE: NONE			SHEET 1 OF 4

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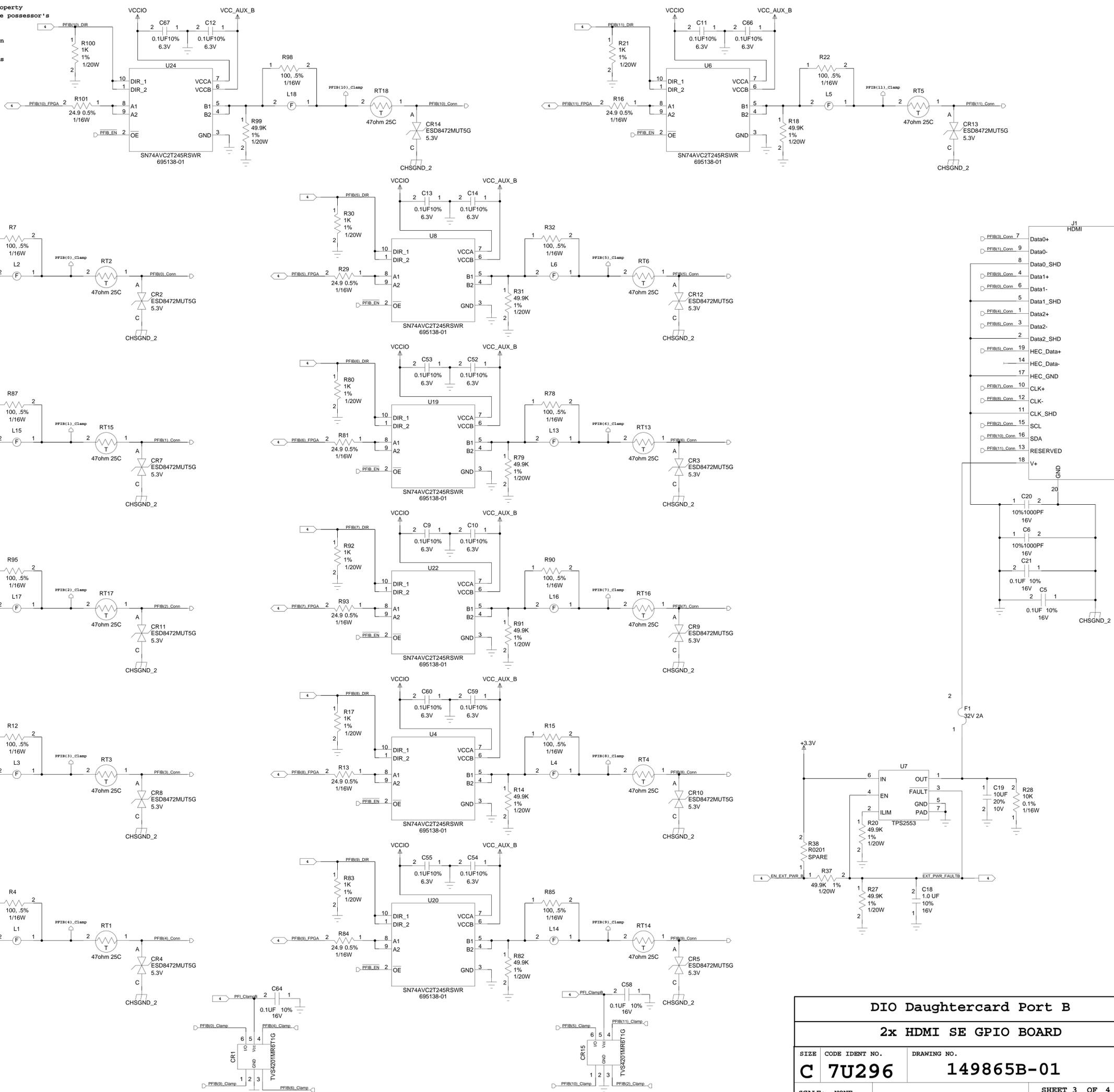


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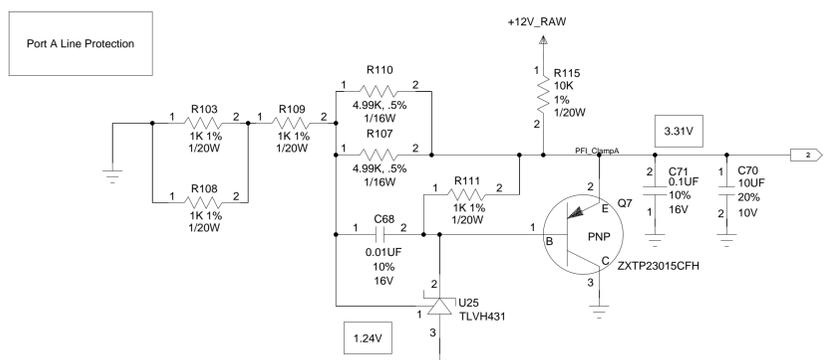
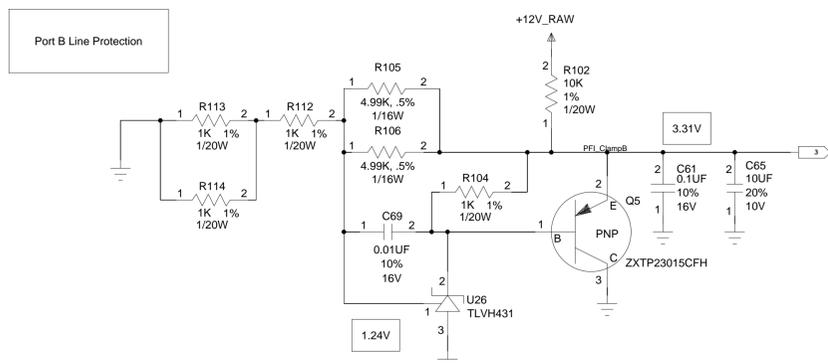


DIO Daughtercard Port B			
2x HDMI SE GPIO BOARD			
SIZE	CODE IDENT NO.	DRAWING NO.	REV
C	7U296	149865B-01	2
SCALE: NONE			SHEET 3 OF 4

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**ID EEPROM I2C Address = 0x50**

